Polynomial complexity optimal multiuser detection for wider class of problems

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Optimal multiuser detection for code division multiple access has complexity which grows exponentially with the number of users. Recently proposed algorithms enable polynomial complexity optimal detection for certain special spreading sequences. It is shown that these algorithms are applicable to a wider class of spreading sequences than initially proposed.

Introduction and motivation: The symbol synchronous code division multiple access (CDMA) system model of [1] is considered, assuming K users, binary modulation and a spreading factor of N. In each symbol period, the maximum likelihood (ML) detector chooses the combination of users' bits b = [b1, ..., bK]T which maximises the conditional probability distribution function of the received signal given b. The matched filter outputs, which form sufficient statistics for making decisions about b based on the received signal, are given by [1]

\[ y = Rb + \nu \]  

where (i) R is the normalised spreading sequence correlation matrix, i.e. R = [r_{ij}], r_{ij} = 1, and r_{ij} \leq 1 for i \neq j, (ii) A is the diagonal matrix of received amplitudes, i.e. A = diag(a_1, ..., a_K) and (iii) w is a jointly Gaussian zero-mean real vector with covariance matrix \( \sigma^2 \) is the noise variance. The ML estimate for b given by [1]

\[ \hat{b}_{ML} = \arg\max_{b \in \{\pm 1\}^K} p_b(y|b) = \arg\max_{b \in \{\pm 1\}^K} (2y^TAb - b^TARAb) \]  

In general, solving (2) requires 2^K function evaluations (for all possible binary vectors of length K) and so the computational complexity grows exponentially, i.e. O(2^K), with the number of users [1].

There exist sets of spreading sequences for which optimum multiuser detection (MUD) can be performed with polynomial complexity (PC).

• If the sequences are orthogonal, i.e. the cross-correlation between distinct pairs of sequences is zero, then optimum detection reduces to solving K single user problems and can be done with linear complexity, i.e. O(K).

• If the sequences are such that the cross-correlation between distinct pairs of sequences is non-negative, then optimal detection requires cubic complexity, i.e. O(K^3) [2, 3].

• If the sequences are such that the cross-correlation between distinct pairs of sequences is constant and we have perfect power control, then optimal detection requires log-linear complexity, i.e. O(K log K) [4].

In [5], it was shown that the approach in [4] is robust to large fluctuations in the users' powers and a modification was presented which improved performance significantly with only a mild increase in complexity. This Letter shows that the PC algorithms in [2-4] are applicable to a wider set of spreading sequences than initially proposed.

Applicability of PC algorithms: In a K user synchronous CDMA system with cross-correlation matrix R and amplitude matrix A, the maximum-likelihood estimate is given by

\[ \hat{b}_{ML} = \arg\max_{b \in \{\pm 1\}^K} (2y^TAb - b^TARAb) \]  

where R does not necessarily satisfy the assumptions made in [2-4], and so these algorithms cannot be directly applied. The following result establishes conditions on R which facilitates the applicability of these algorithms.

Proposition 1: Suppose that \( R = Q'RQ \) where \( Q = \text{diag}(q_1, q_2, ..., q_K) \) and \( q_i \in \{ \pm 1 \} \) for all i. If the off-diagonal elements of R are either (i) strictly non-positive or (ii) constant, then (3) can be solved with polynomial complexity.

Proof: Using the assumptions given, (3) can be rewritten as

\[ \hat{b}_{ML} = \arg\max_{b \in \{\pm 1\}^K} (2y^TQAAb - b^TQ'RAQAb) \]  

where (4) and (5) follow by noting that \( Q^TQ = I \) and QA = AQ, respectively.

Letting \( c = Qb \) and \( z = Qy \), (5) can be written as

\[ \hat{c}_{ML} = \arg\max_{c \in \{\pm 1\}^K} (2c^TAc - c^TARAc) \]  

where the last equality follows from noting that \( Qb \in \{\pm 1\}^K = \{\pm 1\}^T \). The ML solution for b then follows from

\[ \hat{b}_{ML} = Q^T\hat{c}_{ML} \]  

If R satisfies the non-positive cross-correlation property, [2, 3] can be used to solve (6) and equivalently (3) with polynomial complexity. If R satisfies the constant cross-correlation property, it is not necessary that QA = AQ, and there are polynomial complexity solutions in [4].

Comment: In Proposition 1, it was assumed that Q = diag(q_1, q_2, ..., q_K) and \( q_i \in \{ \pm 1 \} \) for all i. However, if there is perfect power control, i.e. \( A = I \), then the only necessary conditions are \( Q'R = Q \) and \( Qb \in \{\pm 1\}^K = \{\pm 1\}^T \). If A \# I, then the assumptions made in Result 1 are necessary because the additional condition that QA = AQ is required. If a square matrix Q commutes with the square diagonal matrix A, then either A is the identity matrix or Q is a diagonal matrix. This is easily seen by equating the (i, i) entries of QA and AQ:

\[ \sum_j Q_{ij}A_{jm} = \sum_j A_{jm}Q_{jm} \]

Since A is diagonal, \( Q_{ij}A_{jm} = A_{jm}Q_{jm} \). The condition i = j yields no constraints. The condition i \# j implies that either \( A_{jm} = 0 \) or \( Q_{jm} = 0 \). So, if A \# I and QA = AQ, then Q = diag(q_1, q_2, ..., q_K).

Finally since \( Q^TQ = I \), it is clear that \( q_i \in \{ \pm 1 \} \) for all i.

Example: Consider the seven-user synchronous CDMA system using the following set of non-orthogonal spreading sequences:

\[ \delta = \begin{bmatrix} -1 & -1 & -1 & 1 & -1 & 1 & 1 \\ -1 & -1 & -1 & 1 & 1 & -1 & 1 \\ -1 & -1 & 1 & 1 & 1 & 1 & -1 \\ 1 & 1 & 1 & 1 & -1 & 1 & 1 \\ 1 & 1 & 1 & 1 & 1 & -1 & 1 \\ 1 & 1 & 1 & 1 & 1 & 1 & 1 \\ 1 & 1 & 1 & 1 & 1 & 1 & 1 \end{bmatrix} \]

Since A is diagonal, Q_{ij}A_{jm} = A_{jm}Q_{jm}. The condition i = j = 1 yields no constraints. The condition i \# j implies that either A_{jm} = 0 or Q_{jm} = 0. So, if A \# I and QA = AQ, then Q = diag(q_1, q_2, ..., q_K). Finally since Q^TQ = I, it is clear that q_i \in \{ \pm 1 \} for all i.
The cross-correlation matrix, $\mathbf{R} = \mathbf{S}^\dagger \mathbf{S}$, is not of the form required by [2-4]. However, it is shown that $\mathbf{R} = \mathbf{Q} \mathbf{R}_Q$, where $\mathbf{Q}$ is the diagonal matrix defined by $\mathbf{Q} = \text{diag} (-1, 1, 1, -1, 1, -1, 1)$, and $\mathbf{R}_Q$ is of the form required by [4], the original problem can be solved with polynomial (log-linear) complexity.

Proposition 2: For any $N$, there exist at least $N$ sequences of length $N$ with the constant cross-correlation property.

Conclusion: In this Letter, it has been shown that the polynomial complexity algorithms in [2-4] are applicable to a wider class of multiuser detection problems. By explicit construction, a lower bound on the size of sequence sets has been presented for which certain optimal polynomial complexity algorithms exist. The low-complexity detection techniques in this Letter are applicable to other maximum-likelihood sequence detection problems, e.g., single-user duobinary signaling.

References

Introduction: The improvement of operation speed of transistors is an essential requirement for over 100 GHz/s-class logic applications. To evaluate the high-speed characteristics of transistors, $f_T$ and $f_{max}$ are commonly used as figures of merit. However, these values are not directly relevant to the performance of digital circuits. High current density capability is also indispensable for ultra-high-speed logic circuits using bipolar transistors. Ring oscillators have been widely used for evaluating device technologies for logic applications. SCF ring oscillators with 6 ps gate delay have been demonstrated using InP HEMTs [1], and ECL ring oscillators in SiGe HBT technology have achieved gate delay of 4.2 ps [2]. Moreover, very short gate delay of 2 ps has been reported for a CML ring oscillator fabricated in a 170 GHz $f_T$, 150 GHz $f_{max}$ InP HBT technology [3]. In the CML oscillator, emitter feedback and zero-peaking techniques [3] made it possible to attain such a small gate delay using production-level HBTs.

This Letter describes 19-stage ECL ring oscillators with ultra-high-speed InP DHBTs with 150 and 200 nm-thick collectors. The operation speed of an ECL gate is less sensitive to fanout than that of a CML gate due to the ECL gate's large current driving capability. Therefore, ECL gates are basically suitable for estimating the operation speed of actual ultra-high-speed logic ICs, which have a fanout greater than one. We used a conventional circuit configuration so as to clarify the potential of the transistor technology.

Table 1: Base layer parameters

<table>
<thead>
<tr>
<th>Thickness (nm)</th>
<th>GaAs mole fraction (collector + emitter)</th>
<th>Short resistance ($\Omega \cdot \mu$m)</th>
</tr>
</thead>
<tbody>
<tr>
<td>CI50-HBT</td>
<td>50.47 → 0.56</td>
<td>573</td>
</tr>
<tr>
<td>C200-HBT</td>
<td>50.47 → 0.60</td>
<td>449</td>
</tr>
</tbody>
</table>

InP DHBT and IC fabrication: The HBT layers were grown on 3-inch InP substrates by low-pressure MOVPE. Collector thicknesses were 150 (CI50-HBT) or 200 nm (C200-HBT). The C150-HBT was designed for high-current-density operation to achieve high transconductance. A step-graded InGaAs/InGaAsP/InP collector [4, 5] was utilized to obtain a high breakdown voltage for such thin collectors. The base layers were compositionally graded InGaAs [6] heavily carbon-doped to 6 × 10^{19} cm⁻³. The thickness, GaAs composition, and sheet resistance of the base layers are shown in Table 1. The InP emitter layer was doped to 3 and 6 × 10^{17} cm⁻³ for the C200- and C150-HBTs, respectively. The C150-HBT was doped to such a high density to compensate for the space charge of electrons in the high-current-density operation. For the device layout, we used the base-pad isolation structure in order to eliminate the extrinsic collector capacitance at the base pad area [7]. The emitter size is 0.8 × 3 μm². The

3.48 ps ECL ring oscillator using over-300 GHz $f_T$/$f_{max}$ InP DHBTs

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An ECL gate delay of 3.48 ps/stage in a 19-stage conventional ring oscillator fabricated in an over-300 GHz $f_T$/$f_{max}$ InP DHBT technology has been demonstrated. This is the first report (to the authors’ knowledge) of sub-4 ps ECL gate delay. Large collector current density contributes to the very short gate delay.

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