The Department of Electrical & Computer Engineering (ECE) at the National University of Singapore (NUS) invites outstanding candidates with an earned Bachelor or Masters degree for a Teaching Assistant position.

Suitable candidates would have experience in one or more of the following areas:

- Embedded system design
- Hardware description language, e.g., VHDL and Verilog
- Backend design flow of ASICs
- FPGA architecture, design algorithms and synthesis
- EDA design algorithms
- Reconfigurable logic
- Digital logic family/memory design

The position requires conducting tutorial classes to undergraduate students, hands-on demonstrations during laboratory sessions and project supervision. The ideal candidate should have some formal teaching experience involving undergraduate students, covering the topics mentioned above. He/she should be fluent in English and possess good knowledge in the Electrical/Electronics Engineering discipline.

This is a 1-year contract position, renewable based on performance. The successful candidate is expected to assume duty before January 2015.

More details about ECE @ NUS can be found at [http://www.ece.nus.edu.sg](http://www.ece.nus.edu.sg)

Interested applicants are invited to send a detailed CV, teaching philosophy statement, University transcript and contact details of 3 referees to:

*Deputy Head (Undergraduate Programs and Student Life) (c/o Ms Jade Ong)*  
*Department of Electrical & Computer Engineering*  
*National University of Singapore*  
*Block E4, Level 4, Room 10 (E4-04-10)*  
*4 Engineering Drive 3, Singapore 117583*  
*Fax: +65 6779-1103*  
*Email: eleopgf@nus.edu.sg*

Only shortlisted candidates will be notified.