

Class Timetable for Schedule 3
Semester 1, AY2017/2018 (generated on Thursday, 17 August, 2017, 11:57)

Day	Start	End	Week	Code	Subject	Group	Type	StaffNames	Venue	Class Status
Mon	09:00	11:00	All	EE2027	ELECTRONIC CIRCUITS	1	L	Hong Minghui	E1-06-09	
Mon	09:00	11:00	All	EE2027	ELECTRONIC CIRCUITS	2	L	Chor Eng Fong	E1-06-01	
Mon	09:00	12:00	All	EE2024	PROGRAMMING FOR COMPUTER INTERFACES	M1	Lab		DigELab-1	
Mon	10:00	11:00	All	EE2025	POWER ELECTRONICS MODULE	T5	T		E5-02-32	
Mon	11:00	12:00	All	EE2020/ EE2026	DIGITAL FUNDAMENTALS/DIGITAL DESIGN	L	L	Chua Dingjuan/ Alioto, Massimo Bruno	LT5	
Mon	11:00	12:00	All	EE2025	POWER ELECTRONICS MODULE	ALL	L	Panda, Sanjib Kumar	LT4	
Mon	13:00	14:00	All	EE2026	DIGITAL FUNDAMENTALS/DIGITAL DESIGN	T1	T		E3-06-12	
Mon	13:00	14:00	All	EE2026	DIGITAL FUNDAMENTALS/DIGITAL DESIGN	T2	T		E3-06-14	
Mon	14:00	17:00	All	EE2020	DIGITAL FUNDAMENTALS/DIGITAL DESIGN	M2	Lab		DigELab-1	
Mon	14:00	17:00		EE2025	POWER ELECTRONICS MODULE	M1	Lab		PowerLab	
Mon	14:00	17:00		EE2025	POWER ELECTRONICS MODULE	M2	Lab		PowerLab	
Mon	14:00	17:00	All	EE2031	CIRCUITS AND SYSTEMS DESIGN LAB	M2	Lab		LinELab	
Tue	09:00	12:00		EE2025	POWER ELECTRONICS MODULE	TU1	Lab		PowerLab	
Tue	09:00	12:00		EE2025	POWER ELECTRONICS MODULE	TU2	Lab		PowerLab	
Tue	09:00	12:00	All	EE2031	CIRCUITS AND SYSTEMS DESIGN LAB	TU1	Lab		LinELab	
Tue	12:00	13:00	All	EE2020	DIGITAL FUNDAMENTALS/DIGITAL DESIGN	T6	T		E3-06-14	
Tue	12:00	13:00	All	EE2024	PROGRAMMING FOR COMPUTER INTERFACES	T1	T		E3-06-15	
Tue	12:00	13:00	All	EE2025	POWER ELECTRONICS MODULE	T1	T		E3-06-11	
Tue	14:00	16:00	All	EE2011	ENGINEERING ELECTROMAGNETICS	1	L	Chen Xudong	E1-06-09	
Tue	14:00	16:00	All	EE2011	ENGINEERING ELECTROMAGNETICS	2	L	Yeo Tat Soon	E3-06-08	
Tue	14:00	16:00	All	EE2011	ENGINEERING ELECTROMAGNETICS	3	L	Guo Yongxin	E5-03-20	
Tue	14:00	16:00	All	EE2011	ENGINEERING ELECTROMAGNETICS	4	L	Chen Zhi Ning	E1-06-08	
Tue	14:00	17:00	All	EE2020	DIGITAL FUNDAMENTALS/DIGITAL DESIGN	TU2	Lab		DigELab-1	
Tue	14:00	17:00	All	EE2032	SIGNAL AND COMMUNICATIONS DESIGN LAB	TU2	Lab		MicWLab	
Tue	15:00	16:00	All	EE2025	POWER ELECTRONICS MODULE	T2	F		E3-06-02	Class closed
Tue	16:00	18:00	All	EE2023	SIGNALS AND SYSTEMS	1	L	Ng Chun Sum	E5-03-20	
Tue	16:00	18:00	All	EE2023	SIGNALS AND SYSTEMS	2	L	Wong Wai Choong, Lawrence	E1-06-09	
Tue	16:00	18:00	All	EE2023	SIGNALS AND SYSTEMS	3	L	Loh Ai Poh/ Tan Woei Wan	E3-06-08	
Tue	16:00	18:00	All	EE2023	SIGNALS AND SYSTEMS	4	L	Zhang Jianwen	E1-06-03	
Wed	09:00	12:00	All	EE2024	PROGRAMMING FOR COMPUTER INTERFACES	W1	Lab		DigELab-1	
Wed	10:00	12:00	All	EE2020/ EE2026	DIGITAL FUNDAMENTALS/DIGITAL DESIGN	L	L	Alioto, Massimo Bruno/ Chua Dingjuan	LT5	
Wed	10:00	12:00	All	EE2025	POWER ELECTRONICS MODULE	ALL	L	Panda, Sanjib Kumar	LT4	
Wed	12:00	13:00	All	EE2024	PROGRAMMING FOR COMPUTER INTERFACES	T3	T		E4-04-04	
Wed	12:00	13:00	All	EE2025	POWER ELECTRONICS MODULE	T4	F		E3-06-14	Class closed
Wed	12:00	14:00	All	EE2027	ELECTRONIC CIRCUITS	1	T		E1-06-09	
Wed	12:00	14:00	All	EE2027	ELECTRONIC CIRCUITS	2	T		E1-06-01	
Wed	13:00	14:00	All	EE2025	POWER ELECTRONICS MODULE	T3	T		E3-06-14	
Wed	13:00	14:00	All	EE2025	POWER ELECTRONICS MODULE	T7	T		E3-06-10	New class
Wed	14:00	15:00	All	EE2024	PROGRAMMING FOR COMPUTER INTERFACES	ALL	L	Tham Chen Khong/ Rajesh Chandrasekhara Panicker	LT6	

Day	Start	End	Week	Code	Subject	Group	Type	StaffNames	Venue	Class Status
Wed	14:00	17:00		EE2025	POWER ELECTRONICS MODULE	W1	Lab		PowerLab	
Wed	14:00	17:00		EE2025	POWER ELECTRONICS MODULE	W2	Lab		PowerLab	
Wed	14:00	17:00	All	EE2031	CIRCUITS AND SYSTEMS DESIGN LAB	W2	Lab		LinELab	
Wed	14:00	17:00	All	EE2032	SIGNAL AND COMMUNICATIONS DESIGN LAB	W2	Lab		MicWLab	
Wed	15:00	16:00	All	EE2020	DIGITAL FUNDAMENTALS/DIGITAL DESIGN	T7	T		E3-06-13	
Wed	16:00	17:00	All	EE2024	PROGRAMMING FOR COMPUTER INTERFACES	T4	T		E3-06-11	
Thu	10:00	11:00	All	EE2026	DIGITAL FUNDAMENTALS/DIGITAL DESIGN	T3	T		E3-06-05	
Thu	10:00	11:00	All	EE2026	DIGITAL FUNDAMENTALS/DIGITAL DESIGN	T4	T		E3-06-04	
Thu	10:00	11:00	All	EE2026	DIGITAL FUNDAMENTALS/DIGITAL DESIGN	T5	T		E3-06-02	
Thu	10:00	12:00	All	EE2023	SIGNALS AND SYSTEMS	1	T		E5-03-20	
Thu	10:00	12:00	All	EE2023	SIGNALS AND SYSTEMS	2	T		E1-06-09	
Thu	10:00	12:00	All	EE2023	SIGNALS AND SYSTEMS	3	T		E3-06-08	
Thu	10:00	12:00	All	EE2023	SIGNALS AND SYSTEMS	4	T		E1-06-03	
Thu	14:00	15:00	All	EE2024	PROGRAMMING FOR COMPUTER INTERFACES	T5	T		E3-06-11	
Thu	14:00	15:00	All	EE2024	PROGRAMMING FOR COMPUTER INTERFACES	T7	T		E3-06-15	New class
Thu	14:00	17:00		EE2025	POWER ELECTRONICS MODULE	TH1	Lab		PowerLab	
Thu	14:00	17:00		EE2025	POWER ELECTRONICS MODULE	TH2	Lab		PowerLab	
Thu	14:00	17:00	All	EE2026	DIGITAL FUNDAMENTALS/DIGITAL DESIGN	TH2	Lab		DigELab-1	
Thu	14:00	17:00		EE2027	ELECTRONIC CIRCUITS	TH2	Lab		LinELab	
Thu	15:00	16:00	All	EE2020	DIGITAL FUNDAMENTALS/DIGITAL DESIGN	T8	T		E3-06-11	
Thu	15:00	16:00	All	EE2025	POWER ELECTRONICS MODULE	T6	T		E3-06-15	Class closed
Thu	15:00	17:00	All	EE2024	PROGRAMMING FOR COMPUTER INTERFACES	ALL	L	Rajesh Chandrasekhara Panicker/ Tham Chen Khong	LT6	
Fri	09:00	12:00	All	EE2024	PROGRAMMING FOR COMPUTER INTERFACES	F1	Lab		DigELab-1	
Fri	09:00	12:00		EE2025	POWER ELECTRONICS MODULE	F1	Lab		PowerLab	
Fri	09:00	12:00		EE2025	POWER ELECTRONICS MODULE	F2	Lab		PowerLab	
Fri	09:00	12:00	All	EE2031	CIRCUITS AND SYSTEMS DESIGN LAB	F1	Lab		LinELab	
Fri	10:00	11:00	All	EE2020	DIGITAL FUNDAMENTALS/DIGITAL DESIGN	T9	T		E3-06-11	
Fri	10:00	11:00	All	EE2024	PROGRAMMING FOR COMPUTER INTERFACES	T6	T		E3-06-13	Class closed
Fri	10:00	12:00	All	EE2032	SIGNAL AND COMMUNICATIONS DESIGN LAB	ALL	L	Yeo Swee Ping/ Zhang Jianwen	E3-06-07	
Fri	14:00	15:00	All	EE2024	PROGRAMMING FOR COMPUTER INTERFACES	T2	T		E3-06-12	
Fri	14:00	16:00	All	EE2031	CIRCUITS AND SYSTEMS DESIGN LAB	ALL	L	Heng Chun Huat	LT7	
Fri	14:00	17:00	All	EE2026	DIGITAL FUNDAMENTALS/DIGITAL DESIGN	F2	Lab		DigELab-1	
Fri	16:00	18:00	All	EE2011	ENGINEERING ELECTROMAGNETICS	1	T		E1-06-04	
Fri	16:00	18:00	All	EE2011	ENGINEERING ELECTROMAGNETICS	2	T		E3-06-09	
Fri	16:00	18:00	All	EE2011	ENGINEERING ELECTROMAGNETICS	3	T		E1-06-05	
Fri	16:00	18:00	All	EE2011	ENGINEERING ELECTROMAGNETICS	4	T		E3-06-08	
Fri	17:00	18:00	All	EE2020	DIGITAL FUNDAMENTALS/DIGITAL DESIGN	T10	T		E3-06-13	

Academic calendar:<http://www.nus.edu.sg/registrar/info/calendar/AY2017-2018.pdf>

Examination calendar:<https://myportal.nus.edu.sg/studentportal/academics/all/semester-1.html>

EE2 Lab Schedule:<http://www.ece.nus.edu.sg/intranet/Students/> [click Second Year: Timetable / Schedule]

Odd weeks (2017) :14 Aug, 28 Aug, 11 Sep, 2 Oct, 16 Oct, 30 Oct, 13 Nov

Recess week (2017) :23 Sep - 1 Oct

Even weeks (2017) :21 Aug, 4 Sep, 18 Sep, 9 Oct, 23 Oct, 6 Nov