Integrated Circuit (IC) DESIGN POSTGRADUATE SCHOLARSHIP (ICPS) PROGRAMME

The Economic Development Board (EDB) of Singapore, in partnership with the University, offers this Joint Industry Postgraduate (JIP) Programme to support full-time students with postgraduate training, to equip them with the critical skills and capabilities in IC Design for Singapore’s electronics industries.

Scholarship
The JIP Programme will provide each successful candidate with:

• A monthly stipend of $2,500, up to a maximum of
  i) 24 months (2 Years) for Master of Engineering (M.Eng) degree;
  ii) 48 months (4 Years) for Doctor of Philosophy degree (Ph.D); or
  upon completion of the degree requirements, whichever is earlier.

• Full-time course fees (including tuition fees, registration fees, examination fees, health insurance and other academic related fees).

Eligibility Criteria
1. Nationality
   The candidate must be a Singapore citizen or a Singapore Permanent Resident (SPR). Non-Singaporeans or non-SPR with excellent background will be considered on case by case basis.

2. Medical & Health Examinations
   The candidate must have passed the medical and health examinations of the University.

3. Acceptance To Other JIP
   A candidate who has accepted offer from any other company-sponsored JIP will not be considered nor selected for this PROGRAMME.

4. Registration period
   The candidate’s date of registration for the programme must be during the period from August 2009 to January 2011.

Other Conditions
1. Bond
   Upon completion of the programme, the successful M.Eng / Ph.D graduate is required to work in a Singapore-based IC company for a duration of two (2) or three (3) years respectively. An M.Eng student who has upgraded to Ph.D will serve three (3) years concurrently covering both the Master and Ph.D degrees.

   A student who is carrying another bond liability (either with the University, MOE or other companies) prior to his / her acceptance into this programme must ensure that the earlier bond obligation does not conflict and is not served concurrently with the EDB’s bond requirements under this programme.

2. Application Deadlines
   Please refer to the timelines at http://www.gse.nus.edu.sg/applications.html.