

Semester 2 Laboratory Schedule (AY2017/2018): < as of 19 Jan 2018 >

Instructional Week	LAB GRP DAY & DATE		CG2023		EE2026	EE2024/EE2028	EE2025	EE2027	EE2031		EE2032	
			AM	PM	PM	AM	PM	PM	AM	PM	AM	PM
1												
2	MON	22-Jan								CSD1		
	TUE	23-Jan										
	WED	24-Jan										SCD1*
	THUR	25-Jan										SCD1*
	FRI	26-Jan							CSD1			SCD1*
3	MON	29-Jan			Lab 1-EE2026					CSD2		
	TUE	30-Jan			Lab 1-EE2026	PCI-1					SCD1*	SCD1*
	WED	31-Jan			Lab 1-EE2026	PCI-1						SCD2*
	THUR	1-Feb				PCI-1						SCD2*
	FRI	2-Feb			Lab 1 : EE2026	PCI-1			CSD2			SCD2*
4	MON	5-Feb			Lab 2-EE2026					CSD3		
	TUE	6-Feb	Expt1 ^		Lab 2-EE2026	PCI-2					SCD2*	SCD2*
	WED	7-Feb			Lab 2-EE2026	PCI-2						SCD3*
	THUR	8-Feb				PCI-2						SCD3*
	FRI	9-Feb	Expt1 ^	Expt1 ^	Lab 2 : EE2026	PCI-2			CSD3			SCD3*
5	MON	12-Feb								CSD4		
	TUE	13-Feb	Expt1 ^								SCD3*	SCD3*
	WED	14-Feb										
	THUR	15-Feb	EVE LUNAR NEW YEAR									
	FRI	16-Feb	LUNAR NEW YEAR									
6	MON	19-Feb			Lab 3-EE2026		Expt1					
	TUE	20-Feb			Lab 3-EE2026	PCI-3	Expt1					
	WED	21-Feb			Lab 3-EE2026	PCI-3	Expt1					
	THUR	22-Feb				PCI-3	Expt1					
	FRI	23-Feb			Lab 3 : EE2026	PCI-3	Expt1		CSD4			
RECESS												
7	MON	5-Mar			Lab 4-EE2026		Expt1			CSD5		
	TUE	6-Mar	Expt2		Lab 4-EE2026	PCI-4	Expt1	BJT				
	WED	7-Mar			Lab 4-EE2026	PCI-4	Expt1	BJT				
	THUR	8-Mar				PCI-4	Expt1	BJT				
	FRI	9-Mar	Expt2	Expt2	Lab 4 : EE2026	PCI-4	Expt1	BJT	CSD5			
8	MON	12-Mar			Proj 1-EE2026					CSD6		
	TUE	13-Mar	Expt2		Proj 1-EE2026	PCI-5		BJT				
	WED	14-Mar			Proj 1-EE2026	PCI-5		BJT				
	THUR	15-Mar				PCI-5		BJT				
	FRI	16-Mar			Proj 1 : EE2026	PCI-5		BJT	CSD6			SCD4
9	MON	19-Mar			Proj 2-EE2026					CSD7		
	TUE	20-Mar	Expt3		Proj 2-EE2026						SCD4	SCD4
	WED	21-Mar			Proj 2-EE2026							SCD4
	THUR	22-Mar										SCD4
	FRI	23-Mar	Expt3	Expt3	Proj 2-EE2026				CSD7			SCD5
10	MON	26-Mar					Expt2			CSD8		
	TUE	27-Mar	Expt3			PCI-6	Expt2				SCD5	SCD5
	WED	28-Mar				PCI-6	Expt2					SCD5
	THUR	29-Mar				PCI-6	Expt2					SCD5
	FRI	30-Mar	GOOD FRIDAY									
11	MON	2-Apr			Proj 3-EE2026		Expt2			CSD9		
	TUE	3-Apr	Expt4		Proj 3-EE2026	PCI-7	Expt2	OPAMP				
	WED	4-Apr			Proj 3-EE2026	PCI-7	Expt2	OPAMP				
	THUR	5-Apr				PCI-7	Expt2	OPAMP				
	FRI	6-Apr	Expt4	Expt4	Proj 3 : EE2026	PCI-7	Expt2	OPAMP	CSD9			
12	MON	9-Apr			Proj 4-EE2026					CSD10		
	TUE	10-Apr	Expt4		Proj 4-EE2026			OPAMP			SCD6	SCD6
	WED	11-Apr			Proj 4-EE2026			OPAMP				SCD6
	THUR	12-Apr						OPAMP				SCD6
	FRI	13-Apr			Proj 4 : EE2026			OPAMP	CSD10			SCD6
13	MON	16-Apr								CSD11		
	TUE	17-Apr				PCI-8						
	WED	18-Apr				PCI-8						
	THUR	19-Apr				PCI-8						
	FRI	20-Apr				PCI-8			CSD11			

## MODULES AND LABORATORIES DETAILS

Module Code and Name	Lab Location
<b>CG2023 Signal &amp; Systems</b>	Digital Systems Application (E4A-04-08)
Lecturers in charge	Experiment / Assignment
Ng CS & Zhang WJ	Expt1 (40) ^ [ @ PC cluster at Blk E2, lvl 3]
Lab Officers	Expt2 (40)
Joseph Ng, Jalil & Raghu	Expt3 (40) Expt4 (40)

Module Code and Name	Lab Location
<b>EE2026 Digital Design</b>	Digital Electronics (E4-03-07)
Lecturers in charge	Experiment / Assignment
Xu YP & Chua DJ	Lab 1 : (60) Getting Started
Lab Officers	Lab 2: (60) Combinational Circuits
Ms MH Chia, Ms Goh KS & Mr FM Ho	Lab 3: (60) Sequential Circuits Part 1 Lab 4: (60) Sequential Circuits Part 2 Proj 1: (60) ADC and VGA on the FPGA Proj 2: (60) Extra Features Proj 3: (60) Debugging Proj 4: (60) Assessment

Module Code and Name	Lab Location
<b>EE2028/EE2028 Prog for Computer Interfaces/Microcontroller Prog Interfacing</b>	Digital Electronics (E4-03-07)
Lecturers in charge	Experiment / Assignment
CK Tham & Rajesh CP	PCI-1: (60) Familiarization with Development Environment
Lab Officers	PCI-2: (60) Assembly Language and C Programming
Ms MH Chia, Ms Goh KS & Mr FM Ho	PCI-3: (60) Assignment 1 Assessment PCI-4: (60) System Design and Baseboard Familiarization PCI-5: (60) Interfacing to Sensors and Output Devices using GPIO, I2C and SPI PCI-6: (60) Interrupt Handling and Event Detection PCI-7: (60) Interfacing to External System using UART PCI-8: (60) Assignment 2 Assessment

Module Code and Name	Lab Location
<b>EE2025 Power Electronics Module</b>	Energy Manegment Microgrid (E3-03- 09/10)
Lecturers in charge	Experiment / Assignment
Liang YC	Expt1
Lab Officers	Expt2
Mr FC Looi & Mr Seow HC	

Module Code and Name	Lab Location
<b>EE2027 Electronic Circuits</b>	Linear Electronics (E4A-06-06)
Lecturers in charge	Experiment / Assignment
Chor EF, Heng CH, Samudra, Chim WK	BJT Amplifier
Lab Officers	OPAMP
Mr Uh CL & Ms Rose Seah	

Module Code and Name	Lab Location
<b>EE2031 Circuits &amp; Systems Design Lab</b>	Linear Electronics (E4A-06-06)
Lecturers in charge	Experiment / Assignment
Chim WK	CSD1: (36) Characterizing diode parameters
Lab Officers	CSD2: (36) Experiment circuits with diodes
Mr Uh CL & Ms Rose Seah	CSD3: (36) Characterizing transistor parameters CSD4: (36) Design filter to meet certain specification CSD5: (36) Oscillator CSD6: (36) Characterizing solar panel CSD7: (36) Gp brain-storming with a given list of components and presenting their ideas CSD8: (36) Mini-project session CSD9: (36) Mini-project session CSD10: (36) Mini-project session CSD11: (36) Group presentation on their mini projects

Module Code and Name	Lab Location
<b>EE2032 Signals &amp; Comm Design Lab</b>	Microwave Teaching Lab (E4-07-08) [Weeks 2*/3*/4* (PC cluster : E1-04-13 & E1-04-14) ]
Lecturers in charge	Experiment / Assignment
Yeo SP & Zhang JW	SCD1: (42) Analog filter design using ADS
Lab Officers	SCD2: (42) AM transceiver design using ADS
Mr Sing CH & Ms Lee SC	SCD3: (42) FM transceiver design using ADS SCD4: (42) Lab on Spectrum SCD5: (42) Lab on Reflection SCD6: (42) Lab on Antenna

Academic calendar: <http://www.nus.edu.sg/registrar/calendar.html>

Examination calendar: <http://www.nus.edu.sg/registrar/event/exam.html>