

Semester 2 Laboratory Schedule (AY2016/2017): < as of 12 Jan 2017 >

WEEK	LAB GRP	DATE	CG2023		EE2020	EE2024		EE2025	EE2031		EE2032		
			AM	PM	PM	AM	PM	PM	AM	PM	AM	PM	
2	MON	16-Jan											
	TUE	17-Jan											
	WED	18-Jan	Engin Day										
	THUR	19-Jan											
	FRI	20-Jan					PCI-1			CSD1			
3	MON	23-Jan			Lab1							SCD1*	
	TUE	24-Jan					PCI-1					SCD1*	
	WED	25-Jan			Lab1		PCI-1			CSD1		SCD1*	
	THUR	26-Jan					PCI-1					SCD1*	
	FRI	27-Jan										SCD1*	
4	MON	30-Jan	LUNAR NEW YEAR										
	TUE	31-Jan					PCI-2						
	WED	1-Feb					PCI-2				CSD2		
	THUR	2-Feb					PCI-2						
	FRI	3-Feb					PCI-2			CSD2			
5	MON	6-Feb			Lab2							SCD2*	
	TUE	7-Feb										SCD2*	
	WED	8-Feb			Lab2					CSD3		SCD2*	
	THUR	9-Feb										SCD2*	
	FRI	10-Feb								CSD3		SCD2*	
6	MON	13-Feb			Lab3								
	TUE	14-Feb	Expt1 ^				PCI-3						
	WED	15-Feb			Lab3		PCI-3				CSD4		
	THUR	16-Feb					PCI-3						
	FRI	17-Feb	Expt1 ^	Expt1 ^			PCI-3			CSD4			
7	MON	27-Feb			Lab4							SCD3*	
	TUE	28-Feb					PCI-4					SCD3*	
	WED	1-Mar			Lab4		PCI-4			CSD5		SCD3*	
	THUR	2-Mar					PCI-4					SCD3*	
	FRI	3-Mar					PCI-4			CSD5		SCD3*	
8	MON	6-Mar			Proj 1								
	TUE	7-Mar	Expt2										
	WED	8-Mar			Proj 1						CSD6		
	THUR	9-Mar											
	FRI	10-Mar	Expt2	Expt2						CSD6			
9	MON	13-Mar			Proj 2							SCD4	
	TUE	14-Mar	Expt3				PCI-5					SCD4	
	WED	15-Mar			Proj 2		PCI-5			CSD7		SCD4	
	THUR	16-Mar					PCI-5					SCD4	
	FRI	17-Mar	Expt3	Expt3			PCI-5			CSD7		SCD4	
10	MON	20-Mar			Proj 3							SCD5	
	TUE	21-Mar					PCI-6					SCD5	
	WED	22-Mar			Proj 3		PCI-6				CSD8	SCD5	
	THUR	23-Mar					PCI-6					SCD5	
	FRI	24-Mar					PCI-6			CSD8		SCD5	
11	MON	27-Mar			Proj 4								
	TUE	28-Mar	Expt4										
	WED	29-Mar			Proj 4						CSD9		
	THUR	30-Mar											
	FRI	31-Mar	Expt4	Expt4						CSD9			
12	MON	3-Apr			Proj 5							SCD6	
	TUE	4-Apr					PCI-7					SCD6	
	WED	5-Apr			Proj 5		PCI-7				CSD10	SCD6	
	THUR	6-Apr					PCI-7					SCD6	
	FRI	7-Apr					PCI-7			CSD10		SCD6	
13	MON	10-Apr											
	TUE	11-Apr					PCI-8						
	WED	12-Apr					PCI-8						
	THUR	13-Apr									CSD11		
	FRI	14-Apr	GOOD FRIDAY					PCI-8			CSD1 1 ⁰⁸		

MODULES AND LABORATORIES DETAILS

Module Code and Name	Lab Location
CG2023 Signal & Systems	Signal & Systems Lab (WS2-03-22) [Week 6 : Expt 1^ (E2-03-09)]
Lecturers in charge	Experiment / Assignment
Ng CS, Zhang JW	Expt1 (40)
Lab Officers	Expt2 (40)
Ms Sai MC	Expt3 (40)
	Expt4 (40)

Module Code and Name	Lab Location
EE2020 Digital Fundamentals	Digital Electronics (E4-03-07)
Lecturers in charge	Experiment / Assignment
Xu YP, Chua DJ	Lab 1: (60) Getting Started
Lab Officers	Lab 2: (60) Combinational Circuits
Ms MH Chia, Ms Goh KS & Mr FM Ho	Lab 3: (60) Sequential Circuits Part 1
	Lab 4: (60) Sequential Circuits Part 2
	Proj 1: (60) Introduction to BASYS3 I/O Devices
	Proj 2: (60) Basic Features Implementation
	Proj 3: (60) Advanced Features Implementation
	Proj 4: (60) Full System Testing
	Proj 5: (60) Project Assessment

CSD1⁰⁸ makeup for Fri (27/1/17, AM) group only due to eve of Lunar New Year

Module Code and Name	Lab Location
EE2024 Prog for Computer Interfaces	Digital Electronics (E4-03-07)
Lecturers in charge	Experiment / Assignment
Rajesh CP, Tham CK	PCI-1: (60) Familiarization with Development Environment
Lab Officers	PCI-2: (60) Assembly Language and C Programming
Ms MH Chia, Ms Goh KS & Mr FM Ho	PCI-3: (60) Assignment 1 Assessment
	PCI-4: (60) System Design and Baseboard Familiarization
	PCI-5: (60) Interfacing to Sensors and Output Devices using GPIO, I2C and SPI
	PCI-6: (60) Interrupt Handling and Event Detection
	PCI-7: (60) Interfacing to External System using UART
	PCI-8: (60) Assignment 2 Assessment

PCI-1 makeup for Fri day group only due to eve of Chinese New Year

Module Code and Name	Lab Location
EE2025 Power Electronics Module	Energy Manegment Microgrid (WS2 #05-20)
Lecturers in charge	Experiment / Assignment
Liang YC	Expt1 (14)
Lab Officers	Expt2 (14)
Mr HC Seow & Mr FC Looi	

Module Code and Name	Lab Location
EE2031 Circuits & Systems Design Lab	Linear Electronics (E4A-06-06)
Lecturers in charge	Experiment / Assignment
Chim WK	CSD1: (36) Characterizing diode parameters
Lab Officers	CSD2: (36) Experiment circuits with diodes
Ms Rose Seah & Mr Uh CL	CSD3: (36) Characterizing transistor parameters
	CSD4: (36) Design filter to meet certain specification
	CSD5: (36) Oscillator
	CSD6: (36) Characterizing solar panel
	CSD7: (36) Gp brain-storming with a given list of components and presenting their ideas
	CSD8: (36) Building the prototype with given list of components
	CSD9: (36) Building the prototype with given list of components
	CSD10: (36) Building the prototype with given list of components
	CSD11: (36) Group presentation on their mini projects

Makeup for Friday group due to "Good Friday" will be available on Tue, Wed & Thur session of week 13.

Module Code and Name	Lab Location
EE2032 Signals & Comm Design Lab	Microwave Teaching Lab (E4-07-12) [Weeks 3*/5*/7* (PC cluster : E1-04-13 & E1-04-14)]
Lecturers in charge	Experiment / Assignment
Yeo SP & Zhang JW	SCD1: (42) Analog filter design using ADS
Lab Officers	SCD2: (42) AM transceiver design using ADS
Mr Sing CH & Ms Lee SC	SCD3: (42) FM transceiver design using ADS
	SCD4: (42) Lab on Spectrum
	SCD5: (42) Lab on Reflection
	SCD6: (42) Lab on Antenna

CSD11⁰⁸ makeup for Fri (14/4/17, AM) group only due to Good Friday

Academic calendar: <http://www.nus.edu.sg/registrar/calendar.html>

Examination calendar: <http://www.nus.edu.sg/registrar/event/exam.html>

Engin Day will be on 18 Jan 2017 (12pm onwards)