

Semester 2 Laboratory Schedule (AY2018/2019): < as of 19 Dec 2018 >

Instructional Week	LAB GRP DAY & DATE	CG2023		CG2028		EE2026		EE2027		EE2028	EE2031	EE2033	PC2020
		AM	PM	AM	PM	AM	PM	AM	PM	AM	PM	AM	PM
1													
2	MON 21-Jan										CSD1		
	TUE 22-Jan											SL0	
	WED 23-Jan											SL0	
	THUR 24-Jan										CSD1	SL0	
	FRI 25-Jan												
3	MON 28-Jan										CSD2		
	TUE 29-Jan						Lab 1					SL1*	
	WED 30-Jan						Lab 1			PCI-1		SL1*	
	THUR 31-Jan					Lab 1	Lab 1				CSD2	SL1*	
	FRI 1-Feb									PCI-1			
4	MON 4-Feb	EVE LUNAR NEW YEAR											
	TUE 5-Feb	LUNAR NEW YEAR											
	WED 6-Feb	LUNAR NEW YEAR											
	THUR 7-Feb												
	FRI 8-Feb												
5	MON 11-Feb										CSD3		pc2020^
	TUE 12-Feb	Expt1					Lab 2					SL2	pc2020^
	WED 13-Feb						Lab 2			PCI-2		SL2	pc2020^
	THUR 14-Feb					Lab 2	Lab 2				CSD3	SL2	pc2020^
	FRI 15-Feb	Expt1	Expt1							PCI-2			
6	MON 18-Feb										CSD4		pc2020^
	TUE 19-Feb	Expt1					Lab 3					SL3	pc2020^
	WED 20-Feb						Lab 3			PCI-3		SL3	pc2020^
	THUR 21-Feb					Lab 3	Lab 3				CSD4	SL3	pc2020^
	FRI 22-Feb	Expt1	Expt1							PCI-3			
7	MON 4-Mar	RECESS											
	TUE 5-Mar	Expt2					Lab 4					SL4	
	WED 6-Mar						Lab 4			PCI-4		SL4	
	THUR 7-Mar					Lab 4	Lab 4				CSD5	SL4	
	FRI 8-Mar	Expt2	Expt2							PCI-4			
8	MON 11-Mar										CSD6		
	TUE 12-Mar	Expt2					Proj 1					SL5	
	WED 13-Mar						Proj 1		MOSFET	PCI-5		SL5	
	THUR 14-Mar					Proj 1	Proj 1				CSD6	SL5	
	FRI 15-Mar	Expt2	Expt2					MOSFET	PCI-5				
9	MON 18-Mar			Expt1							CSD7		
	TUE 19-Mar	Expt3					Proj 2					SL6	
	WED 20-Mar						Proj 2		MOSFET	PCI-6		SL6	
	THUR 21-Mar					Proj 2	Proj 2				CSD7	SL6	
	FRI 22-Mar	Expt3	Expt3		Expt1				MOSFET	PCI-6			
10	MON 25-Mar										CSD8		
	TUE 26-Mar	Expt3					Proj 3					SL7	
	WED 27-Mar						Proj 3			PCI-7		SL7	
	THUR 28-Mar					Proj 3	Proj 3				CSD8	SL7	
	FRI 29-Mar	Expt3	Expt3							PCI-7			
11	MON 1-Apr			Expt2							CSD9		
	TUE 2-Apr	Expt4										SL8	
	WED 3-Apr								OPAMP			SL8	
	THUR 4-Apr										CSD9	SL8	
	FRI 5-Apr	Expt4	Expt4		Expt2					OPAMP			
12	MON 8-Apr			Expt3							CSD10		pc2020
	TUE 9-Apr	Expt4					Proj 4					SL9	pc2020
	WED 10-Apr						Proj 4		OPAMP	PCI-8		SL9	pc2020
	THUR 11-Apr					Proj 4	Proj 4				CSD10	SL9	pc2020
	FRI 12-Apr	Expt4	Expt4		Expt3				OPAMP	PCI-8			
13	MON 15-Apr										CSD11		pc2020
	TUE 16-Apr											SL10	pc2020
	WED 17-Apr											SL10	pc2020
	THUR 18-Apr										CSD11	SL10	pc2020
	FRI 19-Apr	GOOD FRIDAY											

Academic calendar: <http://www.nus.edu.sg/registrars/info/calendar/AY2018-2019.pdf>
 Examination calendar: <https://myportal.nus.edu.sg/studentportal/academics/all/semester-2.html>

MODULES AND LABORATORIES DETAILS

Module Code and Name	Lab Location
CG2023 Signal & Systems	Digital Electronics (E4-03-07)
Lecturers in charge	Experiment / Assignment
Nq CS, Zhang JW & Sangit S	Expt1 (30) ^ @ PC cluster at Blk E2, M3
Lab Officers	Expt2 (30)
Ms MH Chia, Ms Goh KS & Mr FM Ho	Expt3 (30)
	Expt4 (30)

Module Code and Name	Lab Location
CG2028 Computer Organization	Digital Electronics (E4-03-07)
Lecturers in charge	Experiment / Assignment
Rajesh CP & Henry Tan	Expt1 : Assembly Language Programming
Lab Officers	Expt2 : Microarchitecture, Pipelining and Caching
Ms MH Chia, Ms Goh KS & Mr FM Ho	Expt3 : Assignment Assessment

Module Code and Name	Lab Location
EE2026 Digital Design	Digital Electronics (E4-03-07)
Lecturers in charge	Experiment / Assignment
Chua DJ & Massimo	Lab 1 : (60) Getting Started
Lab Officers	Lab 2: (60) Combinational Circuits
Ms MH Chia, Ms Goh KS & Mr FM Ho	Lab 3: (60) Sequential Circuits Part 1
	Lab 4: (60) Sequential Circuits Part 2
	Proj 1: (60) ADC and VGA on the FPGA
	Proj 2: (60) Extra Features
	Proj 3: (60) Debugging
	Proj 4: (60) Assessment

Module Code and Name	Lab Location
EE2028 Microcontroller Prog Interfacing	Digital Electronics (E4-03-07)
Lecturers in charge	Experiment / Assignment
CK Tham & Rajesh CP	PCI-1: (60) Familiarization with Development Environment
Lab Officers	PCI-2: (60) Assembly Language and C Programming
Ms MH Chia, Ms Goh KS & Mr FM Ho	PCI-3: (60) Assignment 1 Assessment
	PCI-4: (60) System Design and Baseboard Familiarization
	PCI-5: (60) Interfacing to Sensors and Output Devices using GPIO, I2C and SPI
	PCI-6: (60) Interrupt Handling and Event Detection
	PCI-7: (60) Interfacing to External System using UART
	PCI-8: (60) Assignment 2 Assessment

Module Code and Name	Lab Location
EE2027 Electronic Circuits	Linear Electronics (E4A-06-03)
Lecturers in charge	Experiment / Assignment
Chor EF & Heng CH	MOSFET
Lab Officers	OPAMP
Mr Uh CL & Ms Rose Seah	

Module Code and Name	Lab Location
EE2031 Circuits & Systems Design Lab	Linear Electronics (E4A-06-03)
Lecturers in charge	Experiment / Assignment
Chim WK	CSD1: (36) Characterizing diode parameters
Lab Officers	CSD2: (36) Experiment circuits with diodes
Mr Uh CL & Ms Rose Seah	CSD3: (36) Characterizing transistor parameters
	CSD4: (36) Design filter to meet certain specification
	CSD5: (36) Oscillator
	CSD6: (36) Characterizing solar panel
	CSD7: (36) Gp brain-storming with a given list of components and presenting their ideas
	CSD8: (36) Mini-project session
	CSD9: (36) Mini-project session
	CSD10: (36) Mini-project session
	CSD11: (36) Group presentation on their mini projects

Module Code and Name	Lab Location
EE2033 Integrated Systems Lab	Linear Electronics (E4A-06-03)
Lecturers in charge	Experiment / Assignment
Heng CH, Luo S & Zhang JW	SL0: Software setup on personal Laptop
Lab Officers	SL1: Measurement of EM spectrum and antenna (* E4-07-08)
Mr Uh CL & Ms Rose Seah	SL2: GNU Radio implementation
	SL3: Opamp GBW and its impact on filter design
	SL4: Filter design and characterization
	SL5: Spectrum analysis and bandwidth investigation
	SL6: OOK modulation and demodulation
	SL7: Mini project session
	SL8: Mini project session
	SL9: Mini project session
	SL10: Mini project evaluation (OOK receiver)

Module Code and Name	Lab Location
PC2020 Electromagnetics for EE	Microwave Teaching Lab (E4-07-08) [Weeks 5/6* (Phy Dept Lab)]
Lecturers in charge	Experiment / Assignment
Chen XD, Yeo TS, Yeo SP, Guo YX	The reflection coefficient and the Smith chart
Lab Officers	
Ms Lee SC, Ms Guo L	