



PHYSICS OF ELECTROTECHNOLOGY
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ESSDERC 2007 Best Paper Award

Dear Ms. Wang:

On behalf of the technical program committee of this year's European Solid-State Device Research Conference ESSDERC 2007 I have much pleasure in informing you that, according to the vote of the participants, the work entitled

"Uniaxial Strained Silicon n-FETs on Silicon-Germanium-on-Insulator Substrates with an e-Si_{0,7}Ge_{0,3} Stress Transfer Layer and Source/Drain Stressors for Performance Enhancement"

and submitted by the authors

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has obtained the **ESSDERC 2007 Best Paper Award**.

Congratulations to you and your coauthors on this great achievement!

The award will be presented to you during the opening ceremony of next year's ESSDERC to be held during Sept. 16-18, 2008 in Edinburgh, U.K.

We are looking forward to seeing you at this event!

Yours sincerely,

Gerhard Wachutka

Local Co-Chairman of ESSDERC 2007